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Education

2010 - 2012
University of Florida
MS Computer and Electrical Engineering

2006 - 2010
West Bengal University of Technology
B.Tech Electronics and Instrumentation Engineering

Certificate
Duke University
Machine Learning for Product Managers

Recent Awards

Division Recognition Award
PLC Framework Leadership
2022
Designing and launching Intel's SW Product Lifecycle (PLC) framework, achieving 100% adoption across 120+ teams.

Division Recognition Award
Security Innovation Award
2024
For building Intel's end-to-end SBOM and traceability system, modernizing software security at scale.

Division Recognition Award
Product Leadership Award
2025
For successfully delivering a compliance-critical product impacting 250+ software offerings under aggressive timelines.

DEVAJEET KAR
SENIOR PRODUCT MANAGER

Summary

Senior Product Manager | 13+ years of experience | **Cross-functional execution** | **Hardware & Software platforms**. Delivered secure, high-quality software and hardware solutions adopted by **127+ teams globally**. Expert in aligning **products with business goals**, fostering **cross-functional collaboration**, and driving customer-focused innovation. Known for acting as the **"Voice of the Customer"** with a strong record in **driving adoption, UX-led delivery**, and **data-informed decision making**.

Core Qualifications

Product & Strategy <ul style="list-style-type: none">Own product roadmap for enterprise-scale platformsDrive full product lifecycle from concept to GTM to sunsetPrioritize features using strategy, data, and customer insightsAlign milestones across engineering, design, and business	User Experience <ul style="list-style-type: none">Lead journey mapping, wireframes, and mockupsDefine scalable, self-serve experiences to drive adoptionBuild onboarding, training, and documentation programsRun A/B tests and feedback loops to refine UX
Agile Execution <ul style="list-style-type: none">Lead Agile development with 100+ cross-functional teamsLaunch features iteratively in fast-paced environmentsExecute customer segmentation and scalable value deliveryFoster collaboration across engineering, design, and GTM teams	Data & Growth <ul style="list-style-type: none">Define KPIs and build dashboards for product performanceUse experiments and analytics to inform decisionsIntegrate compliance, audit readiness, and governanceDrive adoption and growth with go-to-market teams

Tools

Figma | Mural | JIRA | Trello | Qualtrics | Confluence | Google Analytics | Asana | Tableau | Notion | SharePoint

Key Product Achievements

- Enterprise-Scale Product Launch-** Drove cross-functional alignment across 127+ teams to deliver Intel's Long-Term Retention and Support (LTRS) product, enabling compliance with the EU Cyber Resilience Act across 250+ software offerings.
- Security & Automation at Scale-** Led development of a customer-focused BOM platform with automated SBOM generation, cutting vulnerability trace time from months to under 2 hours (300X improvement) and strengthening Intel's software supply chain.
- Compliance Platform Transformation-** Launched a cross-domain compliance platform spanning 14+ regulatory areas; reduced approval timelines by 85% and scaled adoption to 500+ enterprise projects via real-time scoring and metadata governance.
- AI Hardware Leadership-** Delivered Tenstorrent's second-gen AI silicon, resolving critical power, performance, and area (PPA) challenges at 7nm, accelerating product readiness and strengthening market competitiveness.

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SENIOR PRODUCT MANAGER

Professional Experience

Senior Software Product Manager

2021 - Present
Intel Corporation

Long-Term Support & Cyber Resilience Program

- **Define and deliver product roadmap** for Intel's Long-Term Retention and Support (LTRS) product, enabling compliance with the EU Cyber Resilience Act across 250+ software products.
- **Drive cross-functional execution** by aligning engineering, legal, and GTM teams under aggressive compliance deadlines; translated regulatory and customer needs into scalable workflows and intuitive UI specs.
- **Agile & Experimentation-** Embedded agile practices and ran A/B tests to improve iteration speed by 20%, validate product hypotheses, and enhance user experience based on real data.
- **KPI-Driven Impact-** Defined and tracked adoption, velocity, and security KPIs, influencing prioritization and showcasing measurable impact to executive leadership.

Secure Software Delivery: SBOM Generation & Vulnerability Management

- **Platform Development & Scale-** Led the development of Intel's end-to-end SBOM and traceability platform, modernizing vulnerability management across 250+ software products.
- **Automation & Efficiency-** Reduced vulnerability tracking time by 300X through automation and centralized dashboards, improving scale and response time.
- **Customer-Centric Iteration-** Collaborated with OEM partners (e.g., Dell, HPE) to integrate feedback, cutting feature development cycles by 25% and improving adoption.
- **Security & Cost Impact-** Enabled ~\$300M in potential savings by embedding traceability and compliance into the SDLC, boosting audit readiness and security posture.

Compliance Workflow Modernization

- **Compliance Platform Leadership-** Led development of a cross-functional Compliance Intelligence Platform spanning 14+ domains (e.g., Security, Licensing, AI Governance), improving regulatory coverage across key customer segments.
- **Process Optimization-** Reduced compliance approval time from 45 to 7 days by streamlining intake workflows, aligning stakeholders, and integrating compliance into product schedules.
- **Automation & Visibility-** Built automated scoring systems and real-time dashboards, cutting audit prep effort by 80% and scaling adoption to 500+ enterprise projects.
- **Adoption & Data Governance-** Secured adoption from 30+ product teams by driving metadata governance and launching BOM quality tools that improved traceability and data accuracy.

Product Lifecycle Governance Program

- **Lifecycle Governance-** Designed and launched Intel's global product lifecycle governance model, standardizing quality and release checkpoints across 127+ software teams.
- **Cross-Functional Alignment-** Aligned Engineering, Legal, GTM, and Finance on milestone criteria, cutting release delays by 20% and mitigating \$500K+ in downstream risk.
- **Change Management & Adoption-** Drove 100% program adoption within 3 quarters by leading change management and engaging cross-functional stakeholders.
- **Compliance & Audit Readiness-** Improved ISO 9001 audit scores by 80% through lean QA workflows and real-time compliance monitoring across 25 teams.

AI-Powered Search Platform (Pilot Project)

- **AI-Powered Discovery-** Led a pilot to modernize semantic data retrieval using Generative AI, aiming to boost engineering productivity and reduce time-to-market by up to 10x.
- **Product Strategy & Roadmap-** Defined product vision and roadmap for a design knowledge graph and chatbot interface, guiding cross-functional collaboration across engineering, UX, and AI teams.
- **User-Centric Design-** Partnered with designers and engineers to deliver an intuitive chatbot UI, enabling search across 850+ specs with enriched semantic metadata.

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Product Lead – Open Product Framework (OPF) 501(c)

2024-Present
Voluntary Non-profit

- **Founded an open-source platform program** with 75+ deliverables across 5 product pillars.
- Designed scalable content management system using Wix and AI tools.
- Managed program governance including contributor pipeline & peer review process
- Authored comprehensive technical documentation for system architecture and community development.
- Utilized joint-development and interface management skills to collaborate with community and partners.

Hardware Product Manager

2020- 2021
Tenstorrent

- Led strategic planning, project management, and stakeholder communication to manage the convergence of PPA for Tenstorrent's 7nm AI hardware, culminating in the tape-out of the second-generation product.
- Cross-Functional Collaboration: Coordinated with architects, hardware designers, and validation teams to meet product timelines and enhance communication.

Physical Design Lead

2018- 2020
Intel Corporation

- Directed the design and optimization of Intel's 7nm microprocessor, focusing on achieving power, area, and timing targets through innovative approaches to synthesis, floorplanning, placement, and routing.
- Cross-Disciplinary Leadership: Led a team in resolving architectural and reliability challenges, driving successful project delivery through innovation and collaboration.

SCAN RTL Integration Engineer

2012- 2018
Intel Corporation

- Architected and executed Design-for-Test (DFT) solutions for multiple generations of Intel Core processors, achieving industry-leading scan insertion rates and optimizing design turnaround time.
- Achieved 99% scan insertion by addressing complex reset and clock controllability challenges.

Product Management Approach

- **Customer-Centric Mindset-** Drive product decisions using real-world user feedback and continuous discovery.
- **Outcome-Oriented Planning-** Balance long-term vision with short-term iteration to accelerate delivery.
- **Risk & Experimentation-** Manage risk through rapid testing, escalation frameworks, and proactive mitigation.
- **Data-Driven Prioritization-** Use metrics, A/B testing, and usage insights to guide product roadmap decisions.
- **Cross-Functional Leadership-** Align engineering, design, and GTM teams to execute against shared goals.
- **Business Impact Focus-** Deliver scalable, customer-focused solutions that drive measurable outcomes.